

IN THE CLAIMS:

Claims 1, 5, 6, 10, 11, 13, 14, 17, 18, 19 and 20 have been amended herein. All of the pending claims 1 through 20 are presented, pursuant to 37 C.F.R. §§ 1.121(c)(1)(i) and 1.121(c)(3), in clean form below. Please enter these claims as amended. Also attached is a marked-up version of the claims amended herein pursuant to 37 C.F.R. § 1.121(c)(1)(ii).

1. (Amended) A method of assembling a semiconductor device and a lead frame comprising:
forming a lead frame having a plurality of lead fingers, each lead finger of the plurality of lead fingers having a bonding end, the plurality of lead fingers forming an opening at least a size of an attachment surface of a semiconductor device, the lead frame having no die paddle for supporting the semiconductor device thereon;
forming a segment of tape having a shape to fit over at least the bonding end of each lead finger of the plurality of lead fingers of the lead frame forming the opening, the segment of tape extending between the plurality of lead fingers across the opening, the segment of tape having a thermosetting adhesive located in a portion thereof;
adhesively attaching the segment of tape to at least the bonding end of each lead finger of the plurality of lead fingers of the lead frame, the segment of tape providing an attachment location for the semiconductor device through use of the thermosetting adhesive; and
adhesively attaching the semiconductor device to at least a portion of the segment of tape at the attachment location for the semiconductor device using the thermosetting adhesive located on a portion of the segment of tape, the semiconductor device having a portion thereof located within the opening formed by the plurality of lead fingers of the lead frame.

2. The method of claim 1, further including:
forming at least one aperture in the segment of tape.

3. The method of claim 1, further including:
forming a plurality of apertures in the segment of tape.

4. The method of claim 3, wherein the plurality of apertures substantially forms a grid-like pattern of apertures.

5. (Amended) The method of claim 2, wherein the semiconductor device is attached to the segment of tape such that at least a portion of an outer periphery of the semiconductor device is adjacent to a portion of a periphery of the at least one aperture.

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6. (Amended) The method of claim 3, wherein the semiconductor device is attached to the segment of tape such that at least a portion of an outer periphery of the attachment surface of the semiconductor device is positioned within at least one aperture of the plurality of apertures.

7. The method of claim 1, further including:
wire bonding contacts of the semiconductor device to the bonding ends of the plurality of lead fingers.

8. The method of claim 1, further including:
packaging the semiconductor device in an encapsulating material to form a packaged semiconductor device.

9. The method of claim 8, further comprising:
forming the segment of tape to fit within the encapsulating material.

10. (Amended) A method of assembling a semiconductor device and a lead frame comprising:

forming a lead frame having a plurality of lead fingers, each lead finger of the plurality of lead fingers having an end forming an opening between the ends of the plurality of lead fingers of the lead frame having a size of one of at least a size of an attachment surface of a semiconductor device and greater than the attachment surface of the semiconductor device;

forming at least two tape segments shaped to fit over a portion of the ends of the plurality of lead fingers of the lead frame, the at least two tape segments extending across the opening having a size of one of at least the size of the attachment surface of the semiconductor device and greater than the attachment surface of the semiconductor device; and

adhesively attaching the at least two tape segments to the portion of the ends of the plurality of lead fingers, the at least two tape segments being spaced to define at least one opening between the at least two tape segments providing an attachment location for the semiconductor device therein.

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11. (Amended) The method of claim 10, further including forming a plurality of apertures in at least one tape segment of the at least two tape segments.

12. The method of claim 11, wherein the plurality of apertures substantially forms a grid-like pattern of apertures.

13. (Amended) The method of claim 11, further including:
attaching the semiconductor device to the at least one tape segment of the at least two tape segments having at least a portion of an outer periphery of the semiconductor device adjacent to a periphery of at least one aperture of the plurality of apertures.

14. (Amended) The method of claim 13, wherein the semiconductor device is attached to the at least one tape segment such that at least a portion of the outer periphery of the semiconductor device is positioned within the at least one aperture of the plurality of apertures.

15. The method of claim 13, further including:
wire bonding contacts of the semiconductor device to the ends of the plurality of lead fingers.

16. The method of claim 10, further including:
packaging the semiconductor device in an encapsulating material to form a packaged
semiconductor integrated circuit device.

17. (Amended) The method of claim 16, further comprising forming the at least two
tape segments to fit within the encapsulating material.

18. (Amended) The method of claim 10, wherein the at least two tape segments
comprises three or more tape segments.

19. (Amended) The method of claim 10, further including forming at least one
aperture in at least one tape segment of the at least two tape segments.

20. (Amended) The method of claim 10, wherein the adhesively attaching the at least
two tape segments to the portion of the ends of the plurality of lead fingers comprises spacing the
at least two tape segments to define at least two openings between the at least two tape segments.